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PATENT**EXPEDITED PROCEDURE****RESPONSE UNDER 37 CFR 1.116 – EXAMINING GROUP 2811****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
PATENT APPLICATION**

App./Confirm Nos.: 09/683,985 2597

Applicant: Steven Voldman

Filing Date: March 8, 2002

Title: **A METHOD AND STRUCTURE FOR LOW
CAPACITANCE ESD ROBUST DIODES**

Atty. Docket: BUR9-2002-0014-US1

Today's Date: October 11, 2004

Examiner: Ori Nadav

Group Art Unit: 2811

Fax: 703-872-9306

Amendment B**RECEIVED
CENTRAL FAX CENTER****OCT 11 2004**Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Final Office Action dated August 12, 2004, Applicant respectfully requests reconsideration of the outstanding rejections and reexamination of the present application in light of the following remarks and amendments.

CERTIFICATE OF MAILING

I hereby certify that, on the date shown below, this correspondence is being:

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Date: 10/11/04**FACSIMILE**

☒ transmitted by facsimile to the Patent and Trademark Office.

ARNETTE DODGE

Name

Arnette Dodge

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BUR9-2002-0014-US1